High Frequency Transistor NPN Silicon

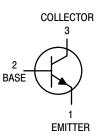
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	VCEO	12	Vdc
Collector-Base Voltage	VCBO	20	Vdc
Emitter-Base Voltage	VEBO	2.5	Vdc
Collector Current — Continuous	IC	50	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	200 1.14	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	300 1.71	mW mW/°C
Storage Temperature Range	T _{stg}	-55 to +150	°C



ON Semiconductor Preferred Device





ELECTRICAL CHARACTERISTICS (T_A = 25° C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Sustaining Voltage $(I_C = 3.0 \text{ mAdc}, I_B = 0)$	VCEO(sus)	12	_	Vdc
Collector–Base Breakdown Voltage ($I_C = 0.001 \text{ mAdc}, I_E = 0$)	V(BR)CBO	20	_	Vdc
Emitter–Base Breakdown Voltage $(I_E = 0.01 \text{ mAdc}, I_C = 0)$	V(BR)EBO	2.5	_	Vdc
Collector Cutoff Current $(V_{CB} = 15 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 15 \text{ Vdc}, I_E = 0, T_A = 150^{\circ}\text{C})$	Ісво		0.02 1.0	μAdc
ON CHARACTERISTICS			•	•
DC Current Coin	b	25	250	

DC Current Gain (I _C = 3.0 mAdc, V _{CE} = 1.0 Vdc)	hFE	25	250	—
Collector–Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc)	VCE(sat)	—	0.4	Vdc
Base–Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc)	V _{BE(sat)}	—	1.0	Vdc

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

MPS5179

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

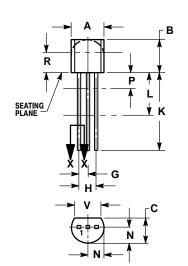
Characteristic	Symbol	Min	Max	Unit
SMALL-SIGNAL CHARACTERISTICS				
Current–Gain — Bandwidth Product ⁽¹⁾ (I _C = 5.0 mAdc, V _{CE} = 6.0 Vdc, f = 100 MHz)	fT	900	2000	MHz
Collector–Base Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 to 1.0 MHz)	C _{cb}	_	1.0	pF
Small Signal Current Gain (I _C = 2.0 mAdc, V _{CE} = 6.0 Vdc, f = 1.0 kHz)	h _{fe}	25	300	—

1. $f_{\mbox{T}}$ is defined as the frequency at which $|h_{\mbox{fe}}|$ extrapolates to unity.

MPS5179

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AL







STYLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
Κ	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

MPS5179

ON Semiconductor and without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan 141–0031 Phone: 81–3–5740–2700 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local Sales Representative.